



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

James W. Adkisson et al.

Serial No. : 09/899,957

Group Art Unit: 2822

Filed: July 6, 2001

Examiner: Trinh, Michael Manh

For: METHOD OF CONTROLLING FLOATING BODY EFFECTS IN AN ASYMMETRICAL SOI DEVICE

Box Non-Fee Amendment
Assistant Commissioner for Patents
Washington, D.C. 20231

AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

This timely responds to the Office Action mailed October 8, 2002.

Please amend the above-identified patent application as follows:

IN THE CLAIMS:

Please cancel claims 1-11. Please amend Claims 12-22 as follows:

12. (Amended) An asymmetric field effect transistor [formed by a process including steps of] comprising:

[defining a gate location with] a trench in a dielectric layer on a semiconductor layer, said semiconductor layer including [supplying] impurities supplied thereto [to said semiconductor layer] at edges of said trench and adjacent source and drain regions, [and forming] wherein the impurities have a precisely desired location, said semiconductor layer being formed on an insulator layer, and a gate structure formed on said semiconductor layer in said trench, with asymmetrical diode properties at the source and drain regions, wherein the transistor is substantially free of floating body effects.

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